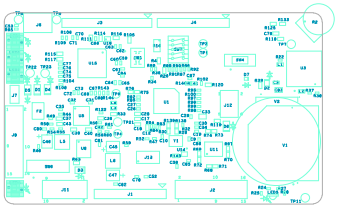
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	ALPHA RELEASE	11/25/15	AKAP
02	BETA RELEASE	12/15/15	AKAP
03	ES10 RELEASE	12/24/15	AKAP
04	ES10 RELEASE	12/28/15	AKAP
05	PRODUCTION RELEASE	01/07/16	AKAP
06	PRODUCTION RELEASE	01/14/16	AKAP
06.1	PRODUCTION RELEASE	01/27/16	AKAP
06.2	PRODUCTION RELEASE	02/02/16	AKAP
07	PRODUCTION RELEASE	02/25/16	AKAP



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES
TOLERANCES ON: ANGLES +/- 2°
2 PL DECIMALS +/- .010
3 PL DECIMALS +/- .005
THIRD ANGLE PROJECTIONS

THIS DOCUMENT CONTAINS CONFIDENTAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY CYPRESS.

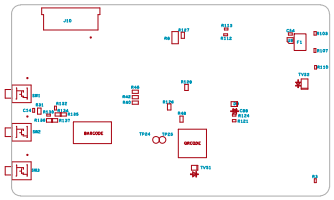
SIGNATURES		DATE
DRAWN	PRAO	02/25/16
CHECKED	AKAP	02/25/16
ENGRG	AKAP	02/25/16
ISSUED		

CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

FABRICATION DRAWING
CY8CKIT-041 PSoC 400S/ 4100S PIONEER KIT

SIZE	FSCM NO	DWG NO	REV
C		620-60304-01	07

SCALE: n/a SHEET:



SECONDARY SIDE

CYPRESS SEMICONDUCTOR

198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

ASSEMBLY DRAWING
CY8CKIT-041-XX-PSoC 4S SERIES PIONEER KIT

SIZE	FSCM NO	DWG NO
C		620-60304-0

SCALE: 1/1

SHEET: 2 OF 2